VLSI-SoC 2022 is the 30th in a series of international conferences sponsored by the International Federation for Information Processing Technical Committee 10 Working Group 5, IEEE CEDA, and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems.

VLSI-SoC 2022 will be held under the theme "SoCs for 5G Evolution and 6G," exploring the design and optimization challenges around 5G+6G communication systems and devices embedded systems, security, low power circuits, powered by CMOS and beyond CMOS-technologies.

While the event is expected to be in presence, its format will be decided at a later date depending on the COVID-19 pandemic evolution.

Topics of interest include, but are not limited to:
- Analog mixed-signal, sensors, and RF
- VLSI circuits and SoC design
- Embedded systems design and software
- EDA tools and methodologies for digital IC design
- Verification, modeling and prototyping
- Design for testability, reliability and fault-tolerance
- Hardware security
- Emerging technologies and new computing paradigms
- AI acceleration
- Approximate Computing
- Circuits for quantum computing
- Cryogenic processors

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Abstract registration: April 18, 2022
Full paper submission: April 25, 2022
Special session proposal: April 25, 2022
Notification of acceptance: June 20, 2022
Camera ready: July 11, 2022